Sean F. Parmenter, Reg. No. 53,437 (925) 472-5000 Attorney Docket No.: 026661-004410US Title: An Analytic Parasitic Constraints Generation Technique Applicant(s): Pengfei Zhang et al. REPLACEMENT SHEET

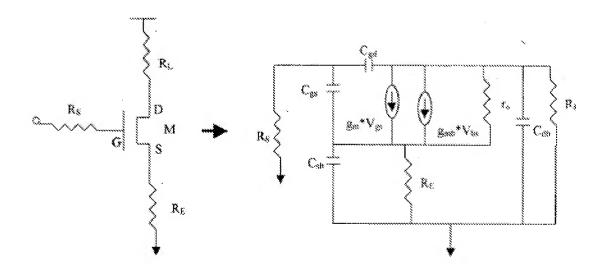


FIG. 1



Title: An Analytic Parasitic Constraints Generation Technique Applicant(s): Pengfei Zhang et al. REPLACEMENT SHEET 200 210 Netlist: GND/PWR Pins, Input Netlist Input/Out Pins, 215 Passive/Active Elements, & Other Terminals 220 · DC Operating Point Simulation Equivalent R Calculation 230 R-C Time-Constants at All Nodes BandWidth Open Circuit Time Constant Estimation 245 240 Whighest=1/RoiCi Module (BandWidth Estimation) Exceptional circuit case will be identified and processed accordingly Design Specifications 255 Comparison 250 Parasitic Loading Constraint Generator Tolerable Parasitic Loading Constants 260 Parasitic Loading Constants

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FIG. 2



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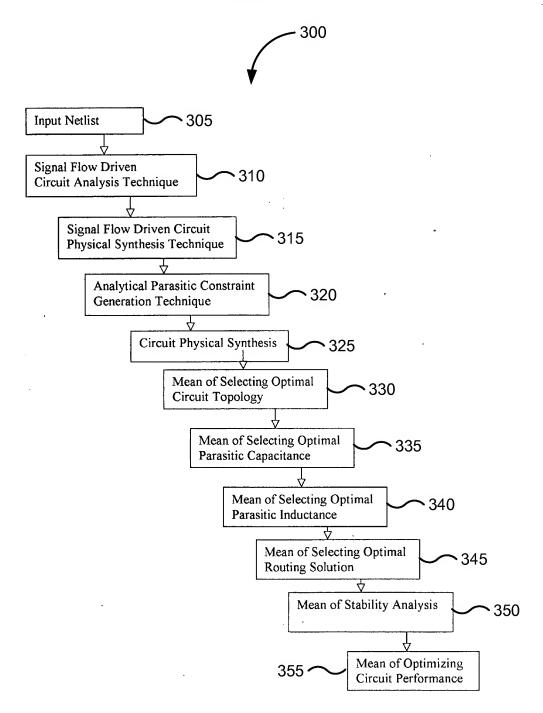


FIG. 3

